10/729, 491.

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TRANSMITTAL		Patent Number		7,144,795 B1		
		Issue Date		December 5, 2006		
FORM		First I	Named Inventor	Terry Lines		
(to be used for all correspondence after initial filing)		Group	Art Unit	2812		
		Exami	ner Name	Walter Lee Lindsay Jr.		
Total Number of Pages in This Submission 5		Attorn	ey Docket Number	100-14320 (P04927-D01)		
ENCLOSURES (check all that apply)						
Fee Transmittal Form		ment Papers Application)		After Al Group	lowance Communication to	
Fee Attached	☐ Drawin	g(s)			Communication to Board of s and Interferences	
Amendment/Response	Licens	Licensing-related Papers			Communication to Group Notice, Brief, Reply Brief)	
After Final (Response)	and Ad	ccompany	Slip (PTO/SB/69) ring Petition	Proprie	tary Information	
Affidavits/declaration(s)		Petition to Convert to a Provisional Application			Letter	
Extension of Time Request		of Attorney, Revocation e of Correspondence Address			Enclosure(s) identify below):	
Express Abandonment Request	☐ Termin			Cer	urn Receipt Postcard tificate of Mailing m PTO-1050 (in duplicate)	
☐ Information Disclosure Statement ☐ CD, No.		umber of	CD(s)			
Certified Copy of Priority Document(s) Rema		ırks				
Response to Missing Parts/ Incomplete Application			•			
Response to Missing Parts under 37 CFR 1.52 or 1.53					Of Correction Certificate Certificate	
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT						
Firm or Individual name Mark C. Pickering, Reg. No. 36,239					Of COV.	
Signature WWW C. F. L.						
Date December 12, 2006						
CERTIFICATE OF MAILING						
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Certificate of Correction Branch, Commissioner for Patents, P.O. Box 1450, Alexandria. VA 22313-1450 on this date:						
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of) Group Art Unit: 2812
Terry Lines) Examiner: Walter Lee Lindsay Jr.
U.S. Pat. No.: 7,144,795 B1)) REQUEST FOR CERTIFICATE OF) CORRECTION OF PATENT FOR PTO MISTAKE
Issued: December 5, 2006	§ 37 CFR 1.322
For: METHOD FOR FORMING A DEPLETION-MODE TRANSISTOR THAT ELMINATES THE NEED TO SEPARATELY SET THE THRESHOLD VOLTAGE OF THE DEPLETION-MODE TRANSISTOR	

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

Attention: Certificate of Correction Branch

Sir:

Attached in duplicate is Form PTO-1050 with at least one copy being suitable for printing. The exact column and line number where the error occurs in the Patent is:

Column 1, Line 13,

After "Field of the Invention", insert new paragraph

--The present invention relates to depletion-mode transistors and, more particularly, to a method of forming a depletion-mode transistor that eliminates the need to separately set the threshold voltage of the depletion-mode transistor.--

Applicant's attorney filed a Preliminary Amendment on December 4, 2003. The Preliminary Amendment inserted a paragraph that identified the related application information. Thus, in view of the Preliminary Amendment, the inserted paragraph that identified the related application information became the first paragraph on page 1. Subsequent to this, applicant's attorney determined that the inserted paragraph that identified the related application information was improperly underlined.

As a result, on February 11, 2004, a Supplemental Preliminary Amendment was filed. The Supplemental Preliminary Amendment deleted the first paragraph of the specification, now the improperly inserted paragraph, and inserted a paragraph in place of the first paragraph that properly identified the related application information (without underlining). Thus, in view of the Supplemental Preliminary Amendment, the properly inserted paragraph became the first paragraph on page 1.

When printing the patent, the USPTO deleted the improperly inserted paragraph, and also erroneously deleted the entire "Field of the Invention" paragraph. As a result, applicant's attorney requests that the "Field of the Invention" paragraph be inserted.

Please send the Certificate to:

Mark C. Pickering, Esq. Law Offices of Mark C. Pickering P.O. Box 300 Petaluma, CA 94953-0300 Customer No. 33402

Dated: 12-12-06

Respectfully submitted,

Mark C. Pickering Reg. No. 36,239

Attorney for Assignee

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page $\underline{1}$ of $\underline{1}$

PATENT NO: 7,144,795 B1

APPLICATION NO.: 10/729491

ISSUE DATE: December 5, 2006

INVENTOR(S): Lines

It is certified that an error appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, Line 13,

After "Field of the Invention", insert new paragraph

--The present invention relates to depletion-mode transistors and, more particularly, to a method of forming a depletion-mode transistor that eliminates the need to separately set the threshold voltage of the depletion-mode transistor.--

MAILING ADDRESS OF SENDER: Mark C. Pickering Law Offices of Mark C. Pickering P.O. Box 300 Petaluma, CA 94953-0300 PATENT NO. 7,144,795 B1 No. of additional copies

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page <u>1</u> of <u>1</u>

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